

## Power compensation for vector-based current control of a modular multilevel converter (MMC) based STATCOM

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### ABSTRACT

This paper studies the suitability of a vector based current control which is mostly employed for its faster response in the reactive current applications of static compensators (STATCOMs). The current control is mostly achieved using proportional-integral (PI) controllers because of the advantage of their good tracking and small variations. However, due to the dependency of PI controllers on the modular multilevel converter (MMC) system dynamics, performance variations arise during steady-state STATCOM non-ideal operations. This paper presents an improved MMC based STATCOM control with a d-q compensation algorithm added to the vector based current control. The algorithm is derived to tackle the effects of the dynamics of the MMC and the STATCOM ideal variations without the use of any additional controller. The control is achieved by providing a power compensation in the d-q axis which injects currents at the input of the PI controllers in order to improve the steady-state performance of the STATCOM control.

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## 1. INTRODUCTION

Power quality can be affected by many types of disturbances which can affect or precisely reduce grid power performance [1]. Depending on the issues arising such as distortions, voltage transients, sags, flickers, swells, unbalances among voltages, and interruptions. Custom power devices (CPDs) technology has been used to mitigate power problems and improve power quality (PQ) [1-3]. Some of these devices such as the universal power quality conditioner (UPQC), static compensator (STATCOM), and dynamic voltage restorer (DVR) can be implemented in voltage source multilevel converters (VSMCs) topologies [4-6]. Shunt devices such as STATCOM which is applied in distribution grids called distribution static compensator (DSTATCOM) exists, it's mainly for reactive power compensation, flicker mitigation, voltage regulation, harmonic filtering, and load voltage balancing. There are series connection devices[7], typical one is dynamic voltage restorer (DVR) commonly used for stabilization of voltage for critical end users. DVR normally comes with an energy facility for storage and short voltage-dip control or short loss of a generation plant from the power network source [7]. Also, there are combined shunt and series connection devices such as universal power quality conditioner (UPQC) [8,9] that can be employed. Electric drives such as driving pumps, high-power consuming blowers, etc. need a high level of electric power conversion for their operation. As the power level reaches many megawatts, their operations at electrical medium voltage (MV) up to 8 kilovolt (kV) has become advantageous option. However, other issues that are of less importance for low-power drives become issues in medium-voltage operations [10, 11]. For instance, at high power and

high-voltage levels, technical issues like, bi-directional power exchange capability or electromagnetic braking in electric drive system is needed when a large quantity of kinetic power is placed in the rotatory bodies. These technical issues can be mitigated by the use of multilevel converters due to their capability to produce less distorted currents and small stepped voltage waveforms with a faster switching. Multilevel inverters have applications which include in flexible A.C transmission systems (FACTS). These applications are possible due to multilevel converters' power switches balancing and distributed voltage stress that are shared among the power switches. The multilevel nature signifies producing nearly stepped sinusoidal output AC voltage waveforms with reduced harmonics. Their principles are based on generating small cascade voltage steps for better power quality, thus, operating at a reduced switching frequency with reduced switching losses, small voltage rise change and better electromagnetic compatibility [12].

There are many time domain based control schemes such as direct and in-direct control, power synchronization, and d-q vector control methods. Among these, most commonly employed are direct p-q and d-q vector control methods in studying the performance of STATCOMs and VSIs. STATCOM applications for power factor support and stability studies have been employed in [13-17] based on different control schemes. Grid integration and VAR support of MMC inverter has been a challenge [18]. Lawan et al. [19] and Mustapha et al. [20] have employed the direct (p-q) approach to improve the grid PCC power factors using MMC based STATCOM applications. Theoretically, no power loss in an ideal STATCOM system. Resulting in no active power transfer through it. In this state the STATCOM is lossless, its output AC voltage will be in phase with the grid AC voltage. But the d-axis current components control is important considering the facts that during transient operations of the STATCOM, due to charging and discharging of the capacitors, there is always a variation in the active power output of the STATCOMs. In other words there is active power transfer between the grid and the STATCOM. In practical, a small amount of power loss is associated with the process [21]. Specifically in the MMC based Inverters and STATCOMs structural topologies. Each sub modules (SM) has cell capacitor with an active energy stored in it or dc-link voltage sources transferred to them. Without the proper capacitors voltage balancing and energy transfer, the MMC based inverters or STATCOMs will always have some variations in their outputs power, low life time of the cells capacitors due to voltage unbalancing and stress etc. [22-24]. Other variations and/or oscillations can occur due to switching or from non-ideal nature capacitors and buffer inductors [25]. A good tuned PI controller could yield a response with a zero or minimum steady state error within a very small rise time.

But with high settling time and overshoots (i.e. higher oscillation period and a longer response) [26-36] and a bit poor dynamics and transient response typical example is in [37]. Another option is the use of P-controllers, even though is simpler in implementation, it has a problem of an increased overshoot compared to PI controllers. This increased overshoot could be seen in [37] where it was employed to synchronize the grid voltage and STATCOM output voltage using decoupled current control. The vector control also employs the cascade control principle. In MMC voltage source inverters, the switching frequency is crucial and restricts how quick the VSI can respond to changes from one operating condition to another, therefore the optimal and achievable inner current control band which is also restricted by the employed switching frequency. That is, the controller bandwidth shows how accurate and fast a control signal could change in time, hence how accurate and fast the MMC system response could be [38]. The MMC based inverter based on the cascade control has the current vector control as the inner current control (ICC), and the outer control that comprises the active and reactive power control. On the other hand, the active power control is usually the active power reference or voltage reference based control. The reactive power control is employed in terms of a reference reactive power or reference AC voltage to provide the desired inner current. The advantage of the cascaded schemes is the increased degree of freedom for the control of the system bandwidth due to the controlled time separation of the loops. The controllers of the inner current control are usually used in VSI are proportional-integral-derivative (PID), hysteresis and ramp time current controllers.

The PID and hysteresis are the most common [38]. The hysteresis current control is the fastest control in terms of dynamic response however gives more distortions due to the low order harmonics, variable switching frequency and effect of dead time [37-38]. In the other word, PI-current controllers have maintained switching frequency but that at the expense of a slower response. The PI-controller has its dynamics depending on the dynamic of the system hence has more controlled coupling effect that gives better distortion. But the hysteresis control does not depend on the dynamics of the system plants, hence has high distortions [37-38] compared to their PI-control counterparts. This proposed d,q-compensation algorithm introduces a transient supports based on the concepts of a transient impacts and applied the concepts of the STATCOM parameter variations to produce an algorithms that enhances the control of the STATCOM. The introduced d,q-transient compensator serving only during the transient operations.

## 2. VECTOR CONTROL FOR MODULAR MULTILEVEL CONVERTER (MMC) INVERTER

In the vector control scheme depicted in Figure 1, the VSC based MMC system in the three phase voltages and currents are transformed into a direct-quadrature (d-q) rotating frame. From Figure 1 and Figure 2 which presents the vector control scheme, applying Kirchhoff's voltage principles, and based on the Laplace transform, the relationship between the grid AC voltage  $\bar{V}_{fdq}$  and the MMC system voltage  $\bar{V}_{cdq}$  of Figure 1 can be written as follows [39-41]:

$$sL_r\bar{I}_{fdq} = \bar{V}_{cdq} - \bar{V}_{fdq} - R_r\bar{I}_{fdq} - j\omega L_r\bar{I}_{fdq} \quad (1)$$

Where:

$R_r$  is the effective resistance between the MMC system and the grid

$L_r$  is the effective inductance between the MMC system and the grid.

$\bar{V}_{fdq}$  is the PCC vector voltage.

$\bar{V}_{cdq}$  is the MMC output vector form of the voltage

$\bar{I}_{fdq}$  is the output vector form of the current at the PCC and  $\omega$  is the fundamental line frequency.

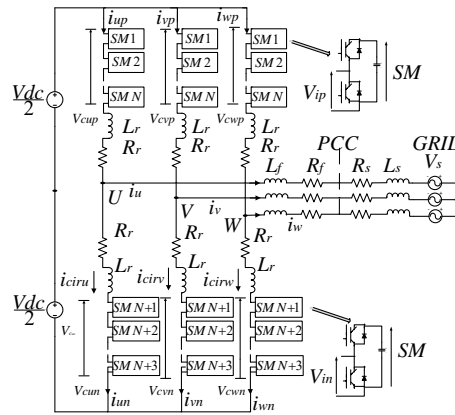


Figure 1. The three phase internal circuitry of a MMC based STATCOM connected to the three phase grid system.

If a constant power grid system is assumed, then the resultant system could be linear and autonomous. Hence the system is assumed to be an independent variable that is independent of time. This assumption is valid since very small deviations of frequency are expected in a healthy and normal operation of a grid power system. These small variations can only happen as grid is expected to have a constant voltage. From Figure 2 (a), a transfer function for the MMC based VIS q-axis voltage can be written as follows:

$$\frac{I_{fq}}{-V_{cq}} = \frac{1}{R_r + sL_r} = \frac{R_r + sL_r}{1 + \frac{(\omega L_r)^2}{(R_r + sL_r)^2} + \omega^2} \quad (2)$$

Where  $I_{fq}$  is the output AC current at the point of coupling (PCC) in the q-axis and  $V_{cq}$  is the MMC system output voltage in the q-axis.

From (2), the poles of the grid connected MMC system will be roots of the system characteristics equation (denominator of (2)):  $(s + R_r / L_r)^2 + \omega^2$ , for  $\omega = 2\pi f$ , where  $f$  is the AC network frequency assumed 50Hz, these poles of the non-feedback system will be complex in nature, hence the system will be oscillatory. In other words, the grid-connected MMC system will have a bad dynamic performance and poorly damped.

To solve this control problem of the non-feedback MMC system, the grid-connected MMC current at the PCC (i.e.  $I_{dL}$  and  $I_{qL}$ ) has to be fed back to the dq-controllers so as to close the current loops. This control improvement is shown in Figure 2 (b). The feedback control loops will improve the system response by shifting the poles of the system open loop transfer function of Figure 2 (a) towards the real axis, thereby pushing the system to have a critically damped response[32]. Assuming the dq-controller to be a simple gain controller (P-controller with  $C(s) = K_p$ ), then block diagram of Figure 2 (b) can be in a transfer function form with outer feedback loops given by:

$$\frac{K_p \cdot \frac{1}{R_T + sL_T}}{1 + K_p \cdot \frac{1}{R_T + sL_T}} = \frac{K_p}{K_p + R_T + sL_T} \approx \frac{1}{1 + sL_T / K_p} \quad (3)$$

Assuming the inner current controller gain is much more higher than the value of the effective resistance between the MMC system and the grid (i.e.  $K_p \gg R_T$ ), then the equivalent representation of the grid connected MMC system in block diagram will be as shown in figure 3 therefore it is possible to derive a q-axis current transfer function given by (4) and (5). Where  $I_{qL}^*$  is the grid –connected MMC system q-axis reference current for the inner current control scheme. The same principles applies to their corresponding d-axis current.

$$\frac{I_{qL}}{I_{qL}^*} = \frac{\frac{1}{1 + s\frac{L_T}{K_p}}}{1 + \left(\frac{\omega L_T}{K_p}\right)^2 \frac{1}{\left(1 + s\frac{L_T}{K_p}\right)^2}} = \frac{1 + s\frac{L_T}{K_p}}{\left(\frac{\omega L_T}{K_p}\right)^2 + \left(1 + s\frac{L_T}{K_p}\right)^2} \quad (4)$$

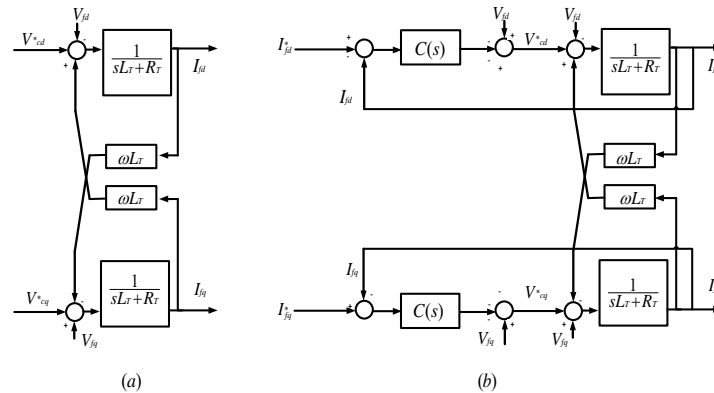


Figure 2. Block diagram representation of the MMC based VSI in a state space equation (a) without feedback controller (b) with feedback controller

$$\frac{I_{qL}}{I_{qL}^*} = \frac{1 + s\frac{L_T}{K_p}}{\left(1 + \frac{\omega L_T}{K_p} + s\frac{L_T}{K_p}\right)\left(1 - \frac{\omega L_T}{K_p} + s\frac{L_T}{K_p}\right)} \approx \frac{1}{1 + s\frac{L_T}{K_p}} \quad (5)$$

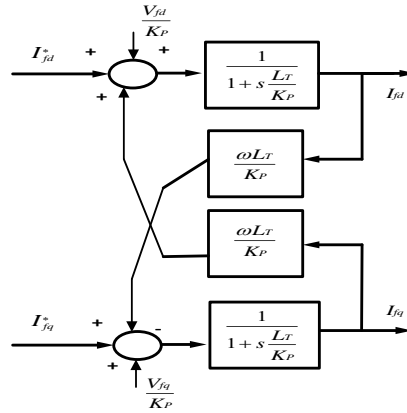


Figure 3. Block diagram representation of the MMC based VSI in a state space equation with decoupled inner current vector controller.

The system obtained from the (5) is a first order system, hence employing a P-controller will improve the system response and helps in the steady state by eliminating the cross coupled interaction between the d-axis and q-axis of the system. The controller will also reduce the effect of the variations in the AC output voltage at the PCC[13]. During the steady state condition (i.e.  $s = 0$ ), for the block diagram shown in figure 3, it can give (6) as:

$$\left. \begin{aligned} \frac{I_q}{I_q^*} &= \frac{1}{1 + \left(\frac{\omega L_r}{K_p}\right)^2} \approx 1, \frac{I_q}{V_q} = \frac{\frac{1}{K_p}}{1 + \left(\frac{\omega L_r}{K_p}\right)^2} \approx \frac{1}{K_p} \text{ \& } \frac{I_q}{I_d^*} = \frac{\frac{\omega L_r}{K_p}}{1 + \left(\frac{\omega L_r}{K_p}\right)^2} \approx \frac{\omega L_r}{K_p} \end{aligned} \right\} \quad (6)$$

Where  $V_q$  is the AC voltage at the PCC in the q-axis and  $I_d^*$  is the AC current at the PCC in the d-axis.

Also from (6), the same principle applies to its corresponding d-axis quantities, where their gains can be found by just substituting the q-axis quantities with the d-axis ones. Analyzing (6), if the term  $\omega L_r / K_p \ll 1$ , the MMC inverter output d,q-currents will follow the reference currents (i.e.  $I_q = I_q^*$  and  $I_d = I_d^*$ ) exactly, hence the effects of the PCC AC voltage variations will be damped by the controller (i.e.  $I_q = V_q / K_p$ ). Also the last term of the (6) implies that the little variations in the d-axis current reference cannot affect the q-axis current in the grid connected MMC.

For a good dynamic performance, if carrier based PWM is employed (i.e.  $I_q = \omega L_r / K_p I_d^*$ ), hence the cross coupling effects of the d-axis and q-axis are cancelled., the closed-loop band width  $\alpha_{cl}$ , should not be greater than 5 times the switching frequency  $\omega_{sw}$ . Also this switching frequency (i.e.  $\omega_{sw}$ ), supposed not be lower than half the sampling frequency  $\omega_s$ , i.e. (7)[32]:

$$\alpha_{cl} \leq \frac{\omega_{sw}}{5} \leq \frac{\omega_s}{5(2)} \quad (7)$$

For instance, if a switching frequency of 2.5 kHz of CSPWM which is 15.70krad/s (50Hz=314 rad/s) is applied, the highest band width of the system  $\alpha_{cl}$ , is estimated as 3.14krad/s (500Hz). Then the rise time,  $t_r$ , which is defined in a first order system as a time the system output goes from 10 to 90% can be defined as (8)[32]:

$$t_r = \ln(9) / \alpha_{cl} \quad (8)$$

The STATCOM device is a shunt device hence connected in parallel a with the grid in the power system via a series impedance  $Z_r$  which contains a coupling inductor and an assumed negligible series resistance at the PCC[6]. The shunt grid connection is employed using the MMC topology is shown in Figure 1 to derive the proposed control schemes. The basic function of the shunt VAR compensation is to

give a power factor enhancement or a voltage support by reducing the voltage drop across the uncertain cumulative buffer and coupling impedances  $Z_c$ . Hence increases the transmissible power along the grid in a power system. The amount of the required power that will produce the required STATCOM current  $i_c$  that flows through the coupling impedance  $Z_c$  is dependent on the difference in voltage between the STATCOM AC output voltage  $V_c$  and the voltage at the PCC  $V_f$ . If the STATCOM is less in voltage than grid PCC  $V_f$ .

The STATCOM absorbs reactive power from the AC power system. Otherwise it injects reactive power. From Figure 1, the  $i_{cq}$  represents the reactive current of the STATCOM while  $i_{cd}$  is the active current used in charging and discharging the two DC-link capacitors.

$$i_f = V_f - V_c \angle \pm \delta \frac{V_f - V_c \angle \pm \delta}{Z_f} \quad (9)$$

Based on the equation (9) using the diagram representations of Figure 1 the transfer function equations of the STATCOM in d,q- representation is given by:

$$\begin{bmatrix} V_{fd} \\ V_{fq} \end{bmatrix} = \begin{bmatrix} V_{fd} \\ V_{fq} \end{bmatrix} + \omega \left( L_f + \frac{L_r}{2} \right) \begin{bmatrix} I_{fd} \\ I_{fq} \end{bmatrix} - \left( R_f + \frac{R_r}{2} \right) \begin{bmatrix} I_{fd} \\ I_{fq} \end{bmatrix} - \left( L_f + \frac{L_r}{2} \right) \frac{d}{dt} \begin{bmatrix} I_{fd} \\ I_{fq} \end{bmatrix} \quad (10)$$

Where  $L_f$  and  $R_f$  are the A.C coupling inductance and resistance respectively while  $L_r$  and  $R_r$  are MMC inverter buffer inductance and resistance. The cumulative MMC inverter to grid connection inductance and resistance is then defined [22] by (11) when the source impedance is neglected.

$$Z_T = L_T + R_T = L_f + \frac{L_r}{2} + R_f + \frac{R_r}{2} \quad (11)$$

This implies that there is total impedance ( $Z_T = L_T + R_T$ ) between the MMC output and the grid. Integrating (10) between the present sample (k) and the next sample (k+1) the average estimated d,q-variables can be as follows:

$$Q_d(k, k+1) = V_{fd}(k, k+1) - V_{cd}(k, k+1) + \omega L_T I_{fq}(k, k+1) - R_T I_{fd}(k) \quad (12)$$

$$Q_q(k, k+1) = V_{fq}(k, k+1) - V_{cq}(k, k+1) - \omega L_T I_{fd}(k, k+1) - R_T I_{fq}(k) \quad (13)$$

Where the relationship between rates of change of the MMC Inverter output dq currents, the effective inductance in which the current changes and d-q variables respectively is defined in (14) and (15).

$$\frac{dI_{fd}}{dt} = \frac{Q_d(k, k+1)}{L_T} \quad (14)$$

$$\frac{dI_{fq}}{dt} = \frac{Q_q(k, k+1)}{L_T} \quad (15)$$

The d,q-variables  $Q_d(k, k+1)$  and  $Q_q(k, k+1)$  from the period of (k) to (k+1) can be got using PI controllers [26] as:

$$Q_{d,q}(k, k+1) = K_{P\_i(d,q)} (I_{f(d,q)}(k+1) - I_{f(d,q)}(k)) + K_{I\_i(d,q)} \int (I_{f(d,q)}(k+1) - I_{f(d,q)}(k)) dt \quad (16)$$

These d,q-variables can be used to obtain their corresponding d,q-voltage components at the MMC inverter during the same sample period ((k) to (k+1)) as follows:

$$V_{cd}(k, k+1) = V_{fd}(k, k+1) + \omega L_T I_{fq}(k, k+1) - R_T I_{fd}(k) - Q_d(k, k+1) \quad (17)$$

$$V_{cq}(k, k+1) = V_{fq}(k, k+1) - \omega L_T I_{fd}(k, k+1) - R_T I_{fq}(k) - Q_q(k, k+1) \quad (18)$$

Where the d, q proportional and integral controllers' gains  $K_{P\_i(d,q)}$  and  $K_{I\_i(d,q)}$  which depend on the MMC based STATCOM dynamics during the d,q current sample rates  $T_{i(d,q)}$  are respectively given by:

$$K_{P\_i(d,q)} = \frac{L_r}{T_{i(d,q)}} + \frac{R_f}{2} \quad (19)$$

$$K_{I\_i(d,q)} = K_{P\_i(d,q)} * T_{i(d,q)} * \frac{R_f}{L_r} \quad (20)$$

These PI controllers fast response makes the STATCOM output d,q current references and the d,q-PI variables  $Q_{(d,q)}(k,k+1)$  of the next sample period (i.e.  $I_{f(d,q)}(k+1)$ ) meet up with the current sample d,q current references  $I_{f(d,q)}^*(k)$  and d,q-PI variables  $Q_{(d,q)}(k)$  respectively as follows:

$$I_{fd}(k+1) = I_{fd}^*(k) \quad (21)$$

$$I_{fq}(k+1) = I_{fq}^*(k) \quad (22)$$

$$Q_{(d,q)}(k,k+1) = Q_{(d,q)}(k) \quad (23)$$

For a linear transition between the two sampling periods of (i.e.  $(k \text{ to } k+1)$ ). (21) and (22) can further be represented in an average form over the time  $(k)$  as:

$$\frac{1}{2} I_{fd}(k) + \frac{1}{2} I_{fd}^*(k) = I_{fd}(k,k+1) \quad (24)$$

$$\frac{1}{2} I_{fq}(k) + \frac{1}{2} I_{fq}^*(k) = I_{fq}(k,k+1) \quad (25)$$

Substituting (21) and (22) into (16) and (17) respectively and using (23) to be represented in a separate d,q form as:

$$Q_d(k) = K_{P\_i(d)}(I_{fd}^*(k) - I_{fd}(k)) + K_{I\_i(d)} \int (I_{fd}^*(k) - I_{fd}(k)) dt \quad (26)$$

$$Q_q(k) = K_{P\_i(q)}(I_{fq}^*(k) - I_{fq}(k)) + K_{I\_i(q)} \int (I_{fq}^*(k) - I_{fq}(k)) dt \quad (27)$$

Due to the action of the PI variables represented in the (24) and (25). The grid d,q voltages and the MMC based STATCOM output d,q voltages are assumed to be the :

$$V_{f(d,q)}(k,k+1) = V_{f(d,q)}(k) \quad (28)$$

$$V_{c(d,q)}(k,k+1) = V_{c(d,q)}^*(k) \quad (29)$$

Putting (21)-(27) into (17) and (18) gives the d,q voltage components at the MMC STATCOM inverter, which serve as the d,q voltage references employed using the PI controllers. These voltage references are defined by:

$$V_{cd}^*(k) = -K_{P\_i(d)}(I_{fd}^*(k) - I_{fd}(k)) - K_{I\_i(d)} \int (I_{fd}^*(k) - I_{fd}(k)) dt + V_{fd}(k) + \frac{\omega L_r}{2}(I_{fq}(k) + I_{fq}^*(k)) - R I_{fd}(k) \quad (30)$$

$$V_{cq}^*(k) = -K_{P\_i(q)}(I_{fq}^*(k) - I_{fq}(k)) - K_{I\_i(q)} \int (I_{fq}^*(k) - I_{fq}(k)) dt + V_{fq}(k) - \frac{\omega L_r}{2}(I_{fd}^*(k) + I_{fd}(k)) - R I_{fq}(k) \quad (31)$$

Equations (30) and (31) which represent the d,q voltage references to the PWM control modulator can be further represented in terms of the estimated d,q variables ( i.e.  $Q_d(k)$   $Q_q(k)$  ) as part of the feed forward terms[26] as:

$$V_{cd}^*(k) = -Q_d(k) + V_{fd}(k) + \frac{\omega L_r}{2} (I_{fq}^*(k) + I_{fd}^*(k)) - R_t I_{fd}(k) \quad (32)$$

$$V_{cq}^*(k) = -Q_q(k) + V_{fq}(k) - \frac{\omega L_r}{2} (I_{fd}^*(k) + I_{fq}^*(k)) - R_t I_{fq}(k) \quad (33)$$

Where  $Q_d(k)$  and  $Q_q(k)$  defined previously in (26) and (27) respectively represent the settled dq variables (i.e.  $Q_d$  and  $Q_q$ ) given in (34) and (35), obtained as outputs of the d,q-PI controllers of a voltage control dynamic model based on the decoupled inner current control of the STATCOM grid connected system[26], shown in Figure 4.

$$Q_d = K_{P\_i(d)} (I_{fd}^* - I_{fd}) + K_{I\_i(d)} \int (I_{fd}^* - I_{fd}) dt \quad (34)$$

$$Q_q = K_{P\_i(q)} (I_{fq}^* - I_{fq}) + K_{I\_i(q)} \int (I_{fq}^* - I_{fq}) dt \quad (35)$$

The reference MMC based STATCOM modulation index  $m$  and phase shift angle  $\delta$  that produce the inverter three phase AC currents (i.e.  $i_k = I \sin(\omega t)$ ) and voltages ( $V_{ck} = V \sin(\omega t + \theta)$ ) has the subscript (i.e.  $k$ ) representing phase number, and  $\theta$  is the phase shift between the output current and voltage in AC form given by (36) and (37) respectively as follows:

$$m = \sqrt{V_{cd}^{*2} + V_{cq}^{*2}} \quad (36)$$

$$\delta = \tan^{-1} \frac{V_{cq}^*}{V_{cd}^*} \quad (37)$$

For the MMC internal control, it employed the common control concepts used in[27][28][24] that utilized the concepts of Carrier shifted pulse width modulation schemes (CSPWM) [6][29][30] [13][12]. Applying the Kirchhoff's theory, the AC voltage in the arm as an output can be derived as follows:

$$V_{ckO} = \frac{1}{2} (V_{ckn} - V_{ckp}) - \frac{1}{2} \left( L_r \frac{di_k}{dt} + R i_k \right) \quad (38), \quad 2 \left( L_r \frac{di_{circ}}{dt} + R i_{circ} \right) = V_{dc} - (V_{ckn} + V_{ckp}) \quad (39)$$

Where  $i_{circ}$  is the circulating current and  $V_{dc}$  is the cumulative dc-link voltage across the arms.  $L_r$  is the buffer arm inductance of each arm.  $R_r$  is the series resistance associated with each buffer inductance that is usually assumed negligible[27].  $L_r$  and  $R_r$  are the AC coupling inductance and its corresponding series resistance respectively. If the MMC is connected to grid, then there will be a source impedance  $R_s$  and  $L_s$  (i.e. the AC source inductance and its corresponding AC source series resistance respectively). The mathematical relationship between the MMC output current of each phase, the arm upper and lower currents, the circulating current and output voltage could be as:

$$i_k = i_{kp} - i_{kn} \quad (40)$$

$$i_{circ} = \frac{1}{2} (i_{kp} + i_{kn}) \quad (41)$$

$$V_{ck} = \frac{1}{2} (V_{ckn} - V_{ckp}) \quad (42)$$

Based on (38) - (39), the regulation of the circulating current  $i_{circ}$  and control of the output voltage  $V_{ck}$  could be achieved by controlling the upper and lower currents and upper and lower voltages. In other word, to achieve the desired output voltage and maintain the balance between the entire DC voltage and each phase leg output voltage, the output voltages can be defined as:



$$V_{ckp} = \frac{1}{2}V_{dc} - V_{ck} \quad (43)$$

$$V_{ckn} = \frac{1}{2}V_{dc} + V_{ck} \quad (44)$$

Where  $V_{ck}$  is the MMC system output voltage of the corresponding phase defined as:

$$V_{ck} = \frac{1}{2}V_{dc} M \sin(\omega t - \theta_k) \quad (45)$$

### 3. THE PROPOSED COMPENSATING DQ-POWER ALGORITHMS ( $Q_{dcom}$ AND $Q_{qcom}$ ) FOR MMC BASED STATCOM DECOUPLED CURRENT CONTROL.

Many attempts to enhance grid voltage or power factor were considered using external reactive currents such as in [6],[31]. These external compensating reactive currents were made from the q-axis components only and introduced as a means of compensating the line load reactive current references. But the d-axis component compensation generation was not considered. Given by (34) and (35) and figure 1, based on the d,q- frame nature of d,q- inner current vector control there will always be the d-axis and q-axis in its implementation. Also considering the facts that the PI controllers are implemented in d,q independent form to control the active and reactive power independently for getting a decoupled dynamic response[22], this work introduced another components (i.e. d-axis active vector compensation components) to compensate for the d-axis segment of the d,q-variable (i.e.  $Q_d$  and  $Q_q$ ), plus the q-axis reactive vector compensation components) to compensate for the q-axis segment of the d,q-variable.

The variables (i.e.  $Q_d$  and  $Q_q$ ) were obtained and applied for the corresponding d,q-PI controllers of the d,q-inner current controllers. This external compensation is important considering the facts that all VSI inverters have either a dc link capacitor(s) or source of dc-link voltages that need an active power control which are mostly carried out by either from set of a reference active power or by the use of a reference voltage to control the dc link voltage as a means of controlling the active power. In other word, the variation of the MMC based STATCOM active power within its cells is also tackled using the d-axis components compensation in this work, considering the widened causes and forms of STATCOM variations. On the other hand, given by (40) and (14) and Figure 8. It can be seen that the extraction of the q-axis current components (i.e. load reactive current  $I_{iq}(k)$ ) is required in maintaining the grid voltage or correct the power factor of the power system at the PCC. This is used as the reactive current reference  $I_{fq}^*(k)$  in order to effectively control the variations. The proposed power algorithm ( $Q_{dcom}$  and  $Q_{qcom}$ ) was derived to basically to produce an opposite d,q- power commands to the d,q- PI controller's output variables ( $Q_d$  and  $Q_q$ ) in response to the STATCOM active and reactive currents variation during the steady state operation. Assuming that in the STATCOM non-ideal operation, the output current has not only the reactive current components  $I_{fq}$  but also has a very small active current components[5] in it its operations  $I_{fd}$  that has some variations.

This small active current variation was considered due to a very small real power exchange assuming a steady state operation of the STATCOM. Therefore, during the transients (i.e. Figure 5), the active current  $I_{fd}$  flows in order to maintain the capacitors voltages during charging and discharging. Hence, a small internal active power loss is associated across the resistance  $R_r$ . Also assuming the power loss due to the voltage drop  $V_L$  across the cumulative inductance  $L_r$  considering the STATCOM operation in quadrant I and quadrant II regions of Figure 5. In these two regions, the STATCOM injects positive reactive power to the grid and absorb a very negligible amount of active power for the dc voltage maintenance respectively. Then the STATCOM can attain a steady state, when compensated and it's the d-axis and q-axis current components  $I_{fd}$  and  $I_{fq}$  in (34) and (35) respectively will be driven to the desired d-axis and q-axis current components  $I_{fd}^*$  and  $I_{fq}^*$ . After the compensation, that is in the steady state, the rate of change of the STATCOM currents  $I_{fd}$  and  $I_{fq}$  will be zero as:

$$\frac{dI_{f(d,q)}}{dt} = 0 \quad (46)$$

Assuming also the cumulative effective resistance  $R_r$  to be negligible during the steady state, and given by:

$$R_f + \frac{R_r}{2} = R_T = 0 \quad (47)$$

Then current components  $I_{fd}^*$  and  $I_{fq}^*$  would have their resultant line current value  $I_f$  in quadrature with the available AC voltage at the grid PCC as shown in figure 1. From the Figure 1 it can be seen that the at the steady state condition, the grid AC voltage  $V_d$  will also be in phase with STATCOM output AC voltage  $V_c$  and the controller at this state, will generate the desired compensating voltage commands with their magnitudes  $V_{fd}^*$  and  $V_{fq}^*$  that can be as:

$$V_{cd}^* = V_{fd} + \omega L_r I_{fq}^* \quad (48)$$

$$V_{cq}^* = \omega L_r I_{fd}^* \quad (49)$$

Considering that in decoupled current control, the voltage vector is always aligned with the d-axis, then grid q-axis voltage vector  $V_{fq}$  can be ignored. Hence  $V_{fq} = 0$ . On the other hand, the grid d-axis voltage vector  $V_{fd}$  is defined in terms of resultant STATCOM d-axis voltage command  $V_{cd}^*$  in (48) based on the opposing voltage drop signal (i.e.  $\omega L_r I_{fq}^*$ ),  $V_{cq}^*$  is the resultant STATCOM q-axis that equals the opposing voltage drop signal  $\omega L_r I_{fd}^*$ .

If from (48) and (49) the corresponding desired d-axis and q-axis STATCOM currents can be defined as:

$$I_{fd}^* = \frac{V_{cq}^*}{\omega L_r} \quad (50)$$

$$I_{fq}^* = \frac{-(V_{cd}^* - V_{fd})}{\omega L_r} \quad (51)$$

Where the cumulative interfacing inductance  $L_r$  is given by:

$$L_r = L_f + \frac{L_r}{2} \quad (52)$$

Their compensating steady-state active and reactive power variables ( $Q_{dcom}$  and  $Q_{qcom}$ ) at the filter PCC bus is as:

$$Q_{dcom} = \frac{3}{2} V_{fd} I_{fd}^* = \frac{3}{2} \frac{V_{cq}^* V_{fd}}{\omega L_r} = V I_{fd}^* \quad (53)$$

$$Q_{qcom} = -\frac{3}{2} V_{fd} I_{fq}^* = \frac{3}{2} \frac{V_{cd}^* V_{fd}}{\omega L_r} - \frac{3}{2} \frac{V_{fd}^2}{\omega L_r} = V I_{fd}^* \quad (54)$$

From (9) supported by figure 4, can be seen that the cumulative inductance  $L_r$  is not only employed for filtering purpose and coupling but is also inevitable for creating a very small phase shift  $\delta$  that will enable the active power exchange during the capacitors transients. On the other hand, from the (9), it can be seen that the voltage difference between the d-axis grid voltage and the d-axis STATCOM voltage represents the voltage difference  $(V_{cd}^* - V_{fd})$  over the cumulative inductance  $L_r$ . Rearranging (54) as shown in (55):

$$Q_{qcom} + \frac{3}{2} \frac{V_{fd}^2}{\omega L_r} = \frac{3}{2} \frac{V_{cd}^* V_{fd}}{\omega L_r} \quad (55)$$

Then the STATCOM resultant output voltage  $V_c^*$  is given as

$$V_c^{*2} = V_{cd}^{*2} + V_{cq}^{*2} \quad (56)$$

Also squaring the (53) and (54) and adding them, as an equation given by (57). (57) will represent a circle equation that presents the MMC inverter active and reactive power capability curve shown in figure 6.

$$Q_{dcom}^2 + \left( Q_{qcom} + \frac{3}{2} \frac{V_{fd}^2}{\omega L_r} \right)^2 = \left( \frac{3}{2} \frac{V_c V_{fd}}{\omega L_r} \right)^2 \quad (57)$$

Figure 6 depicts the regions below and above the active power axis, also shows the under excitation and over citation regions, where the converter injects and absorbs reactive power from and to grid. Therefore for a successful control of an MMC Inverter, the net reactive power  $Q_q$  available for manipulation at the PCC filter bus should be greater than the  $Q_q$  absorbed by the cumulative filter inductance. Based on (57), the cumulative filter inductance  $L_r$  must be selected assuming the active and reactive power MMC inverter capability at a maximum active and reactive power values. Therefore, (58) can be rewritten as:

$$\omega L_r^2 Q_{dmax}^2 + \left( L_r Q_{qmax} + \frac{3}{2} V_{fd}^2 \right)^2 - \left( \frac{3}{2} V_c V_{fd} \right)^2 = 0 \quad (58)$$

For an ideal reactive power STATCOM applications in a steady state, the active power  $Q_d$  in (58) can be considered negligible ( $Q_d \approx 0$ ), therefore (58) can be reduced to:

$$Q_{qmax} = \frac{3}{2} \frac{V_{fd}}{\omega L_r} (V_c - V_{fd}) \quad (59)$$

However, due to the MMC inverter nonlinearity in its typical control operations[13-40], there is always, a small voltage difference between the STATCOM output voltage  $V_c$  and the grid voltage  $V_f$  ( which is assumed to be constant). In other word, the voltage difference  $V_l$  is not exactly equal to zero even during the steady state operation. . To enhance the control in pushing the load line current signals ( $I_{fd}$  and  $I_{fq}$ ) in tracking their desired values  $I_{fd}^*$  and  $I_{fq}^*$ , the proposed d,q algorithm is employed to detects the voltage difference which are proportional to the externally provided proposed d,q-power algorithm variables as a feedforward terms to the decoupling inner current control scheme. These provided d,q power variables are added to the outputs of the d,q-PI controllers in a decoupled form compensating the variables to the STATCOM reactive and active power variations. Note: the active power variation is negligible compared to the reactive power) variations. From (53 and 54), the proposed d,q-power algorithms will die when the STATCOM resultant output voltage  $V_c^*$  has reached its steady state value of unity.

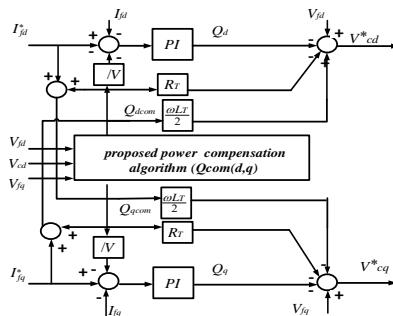
This compensated state would be reached as soon as the dq-power variables have pushed the STATCOM controlled system attained the maximum values available for control at the PCC bus. This coincides with the points in figure 5 where the maximum power is achieved (PF is unity based on the load reactive current) after overcoming the required minimum values for the power absorption in the interfacing reactors. Figure 7 shows the block diagram of the proposed compensating dq power variables for the MMC based STATCOM decoupled inner current control (MMC external control). Finally, the compensating d,q power variables ( $Q_{dcom}$  and  $Q_{qcom}$ ) are then added to the d,q-PI controller terms ( $Q_d$  and  $Q_q$ ) in (32 and 33) respectively to form the final d,q-voltage component references ( $V_{cd}^*(k)$  and  $V_{cq}^*(k)$ ) respectively of the decoupled current vector control for the modulation as follows:

$$V_{cd}^*(k) = -Q_d(k) + Q_{dcom}(k) + V_{fd}(k) + \frac{\omega L_r}{2} (I_{fq}(k) + I_{fq}^*(k)) - R_l I_{fd}(k) \quad (60)$$

$$V_{cq}^*(k) = -Q_q(k) + Q_{qcom}(k) + V_{fq}(k) - \frac{\omega L_r}{2} (I_{fd}^*(k) + I_{fd}(k)) - R_l I_{fq}(k) \quad (61)$$

This transient action would help in tackling the natural poor dynamics of d, q-PI controllers that produced d,q-PI variables therefore, enhancing the overall control system response of the MMC based STATCOM. In other words, the integral controller of the inner current d, q-PI controllers accepts the efforts provided by the transient compensator.

Therefore depressing the contribution effort provided by the external transient state compensator. Sine well designed PI controller's exhibit the properties of improved rise time and also has the integral function to eliminate the steady state error. They have been chosen and employed in this paper for achieving fast and better control of the STACOM reactive and compensated for an improved transient responses.

[illegible]

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#### 4. RESULT AND DISCUSSION

The STATCOM is built based on the MMC topology for a five level operations using a Simulink model and the grid emulator is developed using the parameters shown in Table 1. The control gains for the MMC internal control were also summarized in Table 2. The preliminary setup of the experimental system is underway, built based on the same 5-level phase shifted PWM and balancing technique.

Table 1. Control system parameters of the grid connected mmc based

Symbol	Quantity	Value
$f$	Fundamental frequency	50Hz
$f_{sw}$	Switching frequency	4kHz
$V_f$	Grid voltage	50 V <sub>rms</sub>
$R_s$	Grid resistor	0.500m $\Omega$
$L_s$	Grid inductor	0.120mH
$R_f$	AC coupling resistor	0.100m $\Omega$
$L_f$	AC coupling inductor	2mH
$R_r$	MMC buffer resistor	0.400m $\Omega$
$L_r$	MMC buffer inductor	2mH
$C_e$	Effective capacitor(s)	800uF
$V_{dc}$	DC link voltage	110V
$RL$	Active -inductive Load (kw/Kvar)	500/500
$RC$	Active-capacitive Load (kw/Kvar)	500/500
$K_{P,d,q}$	d-q-axis proportional current gain	2800
$K_{I,d,q}$	d-q-axis Integral inner current gain	300

Table 2. Control system parameters of the capacitor voltage balancing for mmc based statcom system simulation

Symbol	Quantity	Value
$K1$	Proportional inner dc current gain	0.16
$K2$	Integral inner dc current gain	49
$K3$	Proportional outer dc capacitor voltage gain	7.5
$K4$		375
$K5$	Integral outer dc capacitor voltage gain	0.5
$K6$		0.5
	Proportional arm dc voltage gain	
	Proportional individual cap. dc voltage gain	

#### 5. SIMULATION RESULTS

The controllers have a time delay of 200milliseconds and, each MMC cell employed the ideal IGBTs that have no dead time. Since each MMC cell has a carrier frequency of 4 kHz. This means the total switching frequency of the MMC system is 16 kHz (4 kHz times 4). In order to authenticate the proposed control and its theoretical analysis validity, two operating conditions of STATCOM (i.e. capacitive and inductive mode) were considered in these simulations. The active and reactive loads shown in Figure 7 and Figure 8 respectively have better dynamic responses that reached stability at almost 8 cycles before the step change with better responses to the change of the load at 0.4s.

Figure 8 depicts the load reactive power transition from capacitive to inductive to state of both the conventional and The proposed scheme has reached its steady value at about 0.01 seconds from the start of the simulation, but in the conventional scheme, the response has fluctuated before settling at time 0.2 seconds which equivalent to 10 cycles based on the 50Hz used, these responses have shown the superiority of the proposed scheme. Figure 9 shows the load power factor response during the transition from capacitive to inductive state of the MMC based STATCOM operations But the overshoot in the Figure 9 is dominated by the green colour, meaning the overshoot of the conventional is more than that of the proposed scheme at the transition state. The power factor was chosen to be 0.7 for the capacitive and inductive load (500VAR). Figure 11 shows the MMC based STATCOM AC output voltage fast Fourier transform (FFT) analysis for the voltage harmonics using the proposed decoupled inner current control. It can be seen that the proposed control based output voltage has FFT of 1.50 % (Figure 11). While its counterpart in the conventional has 2.07% (Figure 12). Also, from the graphical results, both have shown that at fundamental frequency there is significant level of harmonics magnitude ( i.e. of about more than one (1) magnitude total harmonic distortion (THD)) and almost the same level at second harmonic both with reducing magnitudes, but in the proposed control scheme, there is better level of harmonics. though both have complied with IEEE standard (i.e. less than 5% THD).

Figure 12 depicts the FFT analysis of the controlled capacitors voltage harmonics during the STATCOM and balancing control operations using the proposed decoupled inner current control of the MMC based STATCOM with grid. The total harmonics level in Figure 12 has to be 44.98% THD, the controlled capacitors voltage has shown an overshoot in the response of about 60vdc but settled at about 0.14 seconds which is shorter time.

Figure 13 has shown the FFT analysis of the controlled capacitors voltage harmonics during the STATCOM and balancing control operations based on the conventional scheme with about 77.77% THD (1.7 times 44.98%) which has almost doubled that of the proposed scheme. Also the proposed response has settled at about 0.14 seconds which is faster than the conventional response with about 0.04 seconds (2-50 Hz cycles). From the figures (Figure 12 and 13), it can be seen that the forth order harmonic and above (fifth, sixth, seventh etc.), both the even and odd values are almost zero in both control schemes (i.e. conventional and proposed scheme But for the proposed method, is only the fundamental and second order harmonics are close

to 100 mag. (%of fundamental). In case of the conventional control scheme, the harmonics' presence has reached to the third order level with a value close to also 100 mag. (75 %of fundamental)

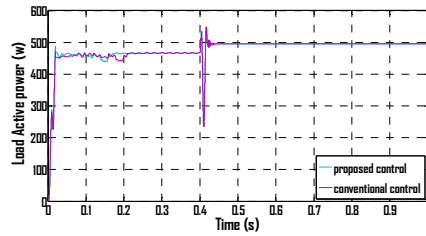


Figure 7. Load active power transition from inductive to capacitive state

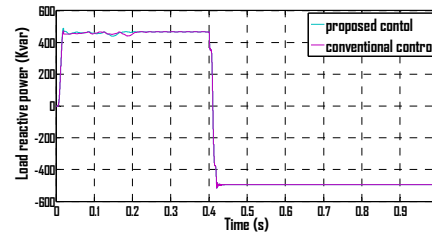


Figure 8. Load reactive power transition from capacitive to inductive state

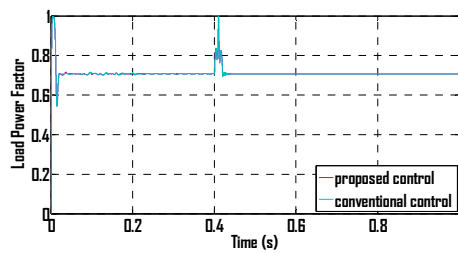


Figure 9. Load power factor during the transition from capacitive to inductive state

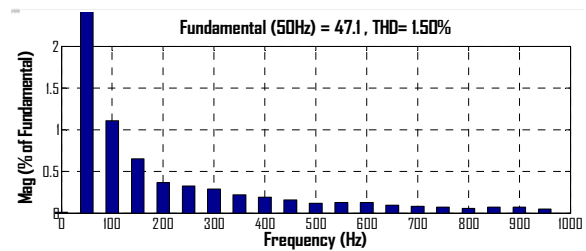


Figure 10. MMC based STATCOM AC output voltage FFT analysis for the voltage harmonics using the proposed decoupled control

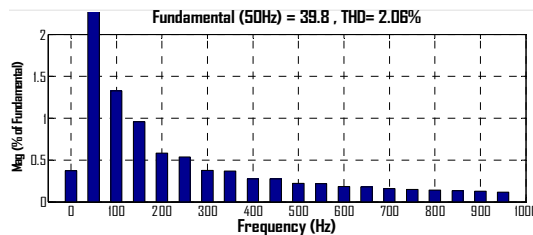


Figure 11. STATCOM AC output voltage FFT analysis for the voltage harmonics using the conventional decoupled inner current control

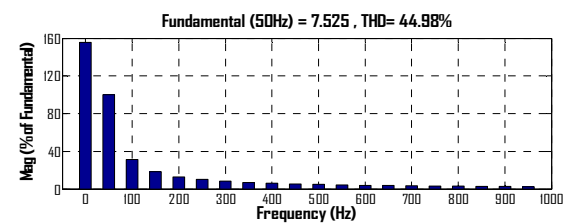


Figure 12. FFT analysis of the controlled capacitors voltage harmonics using the proposed decoupled inner current control of the grid- MMC based STATCOM

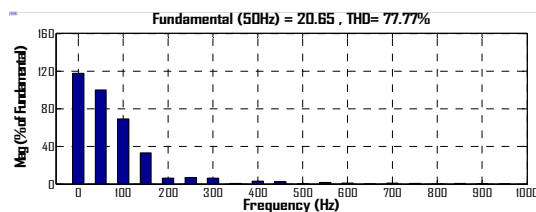


Figure 13. FFT analysis of the controlled capacitors voltage harmonics using the conventional decoupled current control of the grid- MMC based STATCOM



Figure 14. Preliminary Experimental setup of the small proto type Grid-connected Five-Level MMC based STATCOM

## 6. CONCLUSION

This paper has investigated simplicity of carrier-based PWM technique in the STATCOM applications, afterwards, an enhancement on the performance of vector control based MMC is exploited. This has been achieved without the use of additional equipment or use of low switching complicated technique such the SHE. The proposed transient action introduced has helped in tackling the setback of dynamics of PI controllers and the STATCOM non-idealities; thus, it has enhanced the overall control system response of the MMC based STATCOM system. Finally, simulation are presented. In other words, the integral controllers of the inner current PI controllers have accepted the efforts provided by the transient compensators. This development can be used to achieve the practical realization of STATCOM with reduced variations.

## REFERENCES

- [1] L. K. Haw, M. Dahidah, and N. Mariun, "Cascaded multilevel inverter based STATCOM with power factor correction feature," *2011 IEEE Conf. Sustain. Util. Dev. Eng. Technol. STUDENT*, no. October, pp. 1–7, 2011.
- [2] P. Rao, M. L. Crow, and Z. Yang, "STATCOM control for power system voltage control applications," *IEEE Trans. Power Deliv.*, vol. 15, no. 4, pp. 1311–1317, 2000.
- [3] K. Ilango, A. Bhargav, A. Trivikram, P. S. Kavya, G. Mounika, and M. G. Nair, "Power Quality Improvement using STATCOM with Renewable Energy Sources," *Power Electron. (IICPE), 2012 IEEE 5th India Int. Conf.*, p. 1–6, 2012.
- [4] A.U. Lawan, "State Feedback Approaches for Designing A Statcom Supplementary Controller for Oscillations Damping," *Int. J. Eng. Sci.*, vol. 3, no. 7, pp. 27–37, 2014.
- [5] N. Magaji, A. U. Lawan, A. D. O. Dan-isa, and M. W. Mustafa, "Design A STATCOM supplementary Controller for Stability Studies using various state feedback algorithm," *Recent Res. Circuits Syst.*, pp. 38–43, 2012.
- [6] L. K. Haw, M. S. A. Dahidah, S. Member, H. A. F. Almurib, and S. Member, "A New Reactive Current Reference Algorithm for the STATCOM System Based on Cascaded Multilevel Inverters," *IEEE Trans. POWER Electron.*, vol. 30, no. 7, pp. 3577–3588, 2015.
- [7] J. M. Lozano, J. M. Ramirez, and R. E. Correa, "A novel Dynamic Voltage Restorer based on matrix converters," *Proc. - Int. Symp. Mod. Electr. Power Syst. MEPS'10*, no. 1145, pp. 1–7, 2010.
- [8] "Performance Analysis of Interline Unified Power Flowcontroller for parallel transmission lines," *Electr. Comput. Commun. Technol. (ICECCT), 2015 IEEE Int. Conf.*, pp. 1–8, 2015.
- [9] A.U. Lawan, N. Magaji, and H. Musa, "A STATCOM Controller for Small Signal Stability using Polynomial Algorithms in a Horizontal Axis Wind Farm Power System," *2013 IEEE Energytech USA*, pp. 1–5, 2013.
- [10] M. Hagiwara, K. Nishimura, and H. Akagi, "A modular multilevel PWM inverter for medium-voltage motor drives," *2009 IEEE Energy Convers. Congr. Expo. ECCE 2009*, vol. 1, no. c, pp. 2557–2564, 2009.
- [11] G. P. Adam, K. H. Ahmed, S. J. Finney, and B. W. Williams, "Modular multilevel converter for medium-voltage applications," *2011 IEEE Int. Electr. Mach. Drives Conf. IEMDC 2011*, pp. 1013–1018, 2011.
- [12] S. Debnath, J. Qin, B. Bahrani, M. Saeedifard, and P. Barbosa, "Operation, Control, and Applications of the Modular Multilevel Converter: A Review," *IEEE Trans. Power Electron.*, vol. PP, no. 99, pp. 1–1, 2014.
- [13] M. a. Perez, S. Bernet, J. Rodriguez, S. Kouro, and R. Lizana, "Circuit Topologies, Modelling, Control Schemes and Applications of Modular Multilevel Converters," *IEEE Trans. Power Electron.*, vol. PP, no. 99, pp. 1–1, 2014.
- [14] J. Rodríguez, S. Bernet, B. Wu, J. O. Pontt, and S. Kouro, "Multilevel voltage-source-converter topologies for industrial medium-voltage drives," *IEEE Trans. Ind. Electron.*, vol. 54, no. 6, pp. 2930–2945, 2007.
- [15] F. Khoucha, A. Ales, A. Khoudiri, K. Marouani, M. E. H. Benbouzid, and A. Kheloui, "A 7-level single DC source cascaded H-bridge multilevel inverters control using hybrid modulation," *19th Int. Conf. Electr. Mach. ICEM 2010*, no. 1, 2010.
- [16] S. R. Pulikanti and V. G. Agelidis, "Control of neutral point and flying capacitor voltages in five-level SHE-PWM controlled ANPC converter," *2009 4th IEEE Conf. Ind. Electron. Appl. ICIEA 2009*, pp. 172–177, 2009.
- [17] G. Gateau, T. A. Meynard, L. Delmas, and H. Foch, "Stacked multicell converter (SMC): Topology and control," *EPE J. (European Power Electron. Drives Journal)*, vol. 12, no. 2, pp. 14–18, 2002.
- [18] P. Sotoodeh and R. D. Miller, "A single-phase 5-level inverter with FACTS capability using modular multi-level converter (MMC) topology," *Proc. 2013 IEEE Int. Electr. Mach. Drives Conf. IEMDC 2013*, pp. 1229–1234, 2013.
- [19] A.U. Lawan, M. Student, H. Abbas, J. G. K. S. M, and A. A. Karim, "Dynamic performance of Improvement of MMC Inverter with STATCOM Capability interfacing PMSG Wind Turbines with Grid," *2015 IEEE Conf. Energy Convers.*, pp. 492–497, 2015.
- [20] A.U. Lawan, M. Mustapha, I. Abubakar, M. Mustapha, and I. Abubakar, "Reactive current control of STATCOM based MMC Inverter for Wind Turbines connected to Grid," *2015 IEEE Student Conf. Res. Dev.*, pp. 26–31, 2015.
- [21] Historically, "Decoupled Control of Modular Multilevel Converters Using Voltage Correcting Modules," *IEEE Trans. POWER Electron.*, vol. 30, no. 2, pp. 690–698, 2015.
- [22] R. Lizana, S. Member, M. A. Perez, S. Member, D. Arancibia, J. R. Espinoza, and J. Rodriguez, "Decoupled Current Model and Control of Modular Multilevel Converters," *IEEE Trans. Ind. Electron.*, vol. 62, no. 9, pp. 5382–5392, 2015.

- [23] W. Kawamura, M. Hagiwara, and H. Akagi, "Control and experiment of a modular multilevel cascade converter based on triple-star bridge cells," *IEEE Trans. Ind. Appl.*, vol. 50, no. 5, pp. 3536–3548, 2014.
- [24] M. Hagiwara, R. Maeda, and H. Akagi, "Control and analysis of the modular multilevel cascade converter based on double-star chopper-cells (MMCC-DSCC)," *IEEE Trans. Power Electron.*, vol. 26, no. 6, pp. 1649–1658, 2011.
- [25] L. Haw, M. S. a Dahidah, and H. a F. Almurib, "SHE-PWM Cascaded Multilevel Inverter With Adjustable DC Voltage Levels Control for STATCOM Applications," *Power Electron. IEEE Trans.*, vol. 29, no. 12, pp. 6433–6444, 2014.
- [26] S.A. Azmi, G. P. Adam, K. H. Ahmed, S. J. Finney, and B. W. Williams, "Grid interfacing of multimegawatt photovoltaic inverters," *IEEE Trans. Power Electron.*, vol. 28, no. 6, pp. 2770–2784, 2013.
- [27] M. Zhang, L. Huang, W. Yao, and Z. Lu, "Circulating harmonic current elimination of a CPS-PWM-based modular multilevel converter with a plug-in repetitive controller," *IEEE Trans. Power Electron.*, vol. 29, no. 4, pp. 2083–2097, 2014.
- [28] M. Hagiwara, K. Nishimura, and H. Akagi, "A medium-voltage motor drive with a modular multilevel PWM inverter," *IEEE Trans. Power Electron.*, vol. 25, no. 7, pp. 1786–1799, 2010.
- [29] J. I. Y. Ota, Y. Shibano, and H. Akagi, "A Phase-Shifted PWM D-STATCOM Using a Modular Multilevel Cascade Converter (SSBC) — Part II: Zero-Voltage-Ride-Through Capability," *IEEE Trans. Ind. Appl.*, vol. 51, no. 1, pp. 289–296, 2015.
- [30] J. I. Y. Ota, Y. Shibano, N. Niimura, and H. Akagi, "A Phase-Shifted-PWM D-STATCOM Using a Modular Multilevel Cascade Converter (SSBC): Part I. Modeling, Analysis, and Design of Current Control," *IEEE Trans. Ind. Appl.*, vol. 9994, no. c, pp. 1–1, 2014.
- [31] K. H. Law, "New Current Control Algorithm Incorporating Multilevel SHE-PWM Approach for STATCOM Operation under Unbalanced Condition," *Power Electron. Distrib. Gener. Syst. (PEDG), 2014 IEEE 5th Int. Symp.*, pp. 1–7, 2014.
- [32] D. E. Soto-Sanchez, R. Pena, R. Cardenas, J. Clare, and P. Wheeler, "A cascade multilevel frequency changing converter for high-power applications," *IEEE Trans. Ind. Electron.*, vol. 60, no. 6, pp. 2118–2130, 2013.
- [33] R. Majumder, "Reactive Power Compensation in Single Phase Operation of MicroGrid," *IEEE Trans. Ind. Electron.*, vol. 60, no. 4, pp. 1403–1416, 2012.
- [34] M. Miranbeigi, Y. Neyshabouri, and H. Iman-Eini, "State feedback control strategy and voltage balancing scheme for a transformer-less STATIC synchronous COMPensator based on cascaded H-bridge converter," *IET Power Electron.*, vol. 8, no. 6, pp. 906–917, 2015.
- [35] Oates and G. Mondal, "DC circulating Current for Capacitor Voltage Balancing in Modular Multilevel Matrix Converter," *Proc. 2011 14th Eur. Conf. Power Electron. Appl.*, pp. 1–6, 2011.
- [36] W. L. Chen and C. Z. Xie, "Active voltage and frequency regulator design for a wind-driven induction generator to alleviate transient impacts on power grid," *IEEE Trans. Ind. Electron.*, vol. 60, no. 8, pp. 3165–3175, 2013.
- [37] H. Daniyal, E. Lam, L. J. Borle, and H. H. C. Iu, "Hysteresis, PI and Ramp-time Current Control Techniques for APF: An experimental comparison," *Proc. 2011 6th IEEE Conf. Ind. Electron. Appl. ICIEA 2011*, pp. 2151–2156, 2011.
- [38] AU Lawan, HAF Almurib, JG Khor, S Babani "Circulating Current control of a Modular Multilevel Converter (MMC) with State Feedback Controller and Harmonic Current Suppression " *2018 IEEE International Conference on Electrical Systems for Aircraft, Railway, Ship Propulsion and Road Vehicles & International Transportation Electrification Conference (ESARS-ITEC)*, pp 1-8, 2018.
- [39] Syed Zulqadar Hassan, Hui Li, Tariq Kamal, Ali Arsalan, Abdurrahman Umar Lawan "Performance of grid-integrated Wind/Microturbine/battery Hybrid renewable power system" *Power and Energy (PECon), 2016 IEEE International Conference*, pp 78-83, 2016.
- [40] Abubakar Abdulkarim, Sobhy Mohammed Abdelkader, D John Morrow, Falade John Alaba, Abdurrahman Umar Lawan, HR Iswadi "Effect of weather and the hybrid energy storage on the availability of standalone microgrid" *International Journal of Renewable Energy Research (IJRER)*, Vol. 6, no. 1, pp. 189-198, 2016.
- [41] Abdurrahman Umar Lawan, Haider A.F. Almurib, Jeen G. Khor "Enhanced decoupled Current Control with Voltage Compensation for Modular Multilevel Converter (MMC) based STATCOM" *International Journal of Power Electronics and Drives System (IJPEDS)*, Vol. 10, no. 3, pp. 24-25, 2019.